UHD-SDI

FEATURING New UHD-SDI Devices

Broadcast Video Selector Guide

- Equalizers
- Cable Drivers
- Reclockers
- Configurable Equalizer/Driver Devices
- HDMI/SDI Bridge
- Transmitters/Serializers
- Receivers/Deserializers
- Crosspoint Switches
- Timing

semtech.com
High-Performance

UHD-SDI Products

Semtech’s UHD-SDI products extend a 20+ year leadership in SDI technology with a complete line of advanced chipsets. Semtech’s multi-rate device lineup enables differentiated and advanced baseband video products. We offer the world’s most advanced devices designed specifically for real world broadcast challenges, including the latest innovations that help push the boundaries of performance, reach, power, and signal integrity while reducing time to market and design risks.

Comprehensive portfolio of industry-leading UHD-SDI products

We offer the most comprehensive, end-to-end portfolio of UHD-SDI video products available, including our new family of long-reach, dual-input, multi-rate 12G retiming equalizers and cable drivers, low power 3G equalizers, cable drivers and reclockers, integrated adaptive cable equalizers and cable drivers, and innovative HDMI/SDI bridge devices.
UHD-SDI: The Next Generation Broadcast Television Production Interface

New UHD-SDI solutions are needed to enable next-generation broadcast television and D-Cinema applications. UHD-SDI is fully standardized for next-generation broadcast television production of high definition television (HDTV), ultra-high definition television (UHDTV), high dynamic range (HDR), high frame rate (HFR), and wide color gamut (WCG) services.

Dedicated to customer success

Our commitment to customer success drives everything we do. We are unique in providing:

- Comprehensive testing for each component in production, assuring high yield on assembled boards
- Complementary design support, including review and feedback to shorten development cycles, reduce risks and optimize performance
- Dedicated field applications engineering support throughout the product’s life cycle
Equalizers

Integrated reclocking, low power, long reach, and support for data rates up to 11.88Gbps

**SUPPORT UHD-SDI RATES UP TO 12G**
Semtech offers multi-rate adaptive cable equalizers that support rates up to 11.88Gbps (12G) while continuing to offer industry-leading performance at 6G, 3G, HD, and SD rates. Semtech’s GS12341 adaptive cable equalizer features an integrated reclocker to maximize performance, and a new design which offers superb performance in the presence of UHD-SDI stress patterns. In addition to robust performance, Semtech adaptive cable equalizers comply to SMPTE ST 2082-1, ST 2081-1, ST 424, ST292-1, and ST 259-C standards.

**LOW POWER AND SMALL PACKAGE SIZE**
PCB board density continues to increase, requiring small package sizes and low power. Semtech’s GS12341 adaptive cable EQ with integrated reclocking features a 6x4 QFN package. This manufacturing-friendly asymmetric package facilitates high density designs by accommodating a dense connector pitch with its narrow 4mm package width.

In addition, Semtech features a range of adaptive cable equalizers for applications requiring low power. The GS3140 low power, multi-rate 3G adaptive cable equalizer is ideal for applications requiring both low power and long cable reach.

**ADVANCED SIGNAL INTEGRITY DIAGNOSTICS**
The GS12341 features unique diagnostic capabilities, including pattern generation and error checking, and a high-resolution 3D eye monitor to ease board design and debugging while speeding time to market.

**INTEGRATED RELOCKING, LOWEST JITTER**
The GS12341’s integrated reclocker, with full bandwidth clock division, allows low output jitter, while maintaining industry leading input jitter tolerance, even at extended cable lengths and at all supported data rates.

**CROSSTALK INTERFERENCE ROBUSTNESS**
The advanced design minimizes the effect of inter-channel crosstalk interference in high channel density applications.

<table>
<thead>
<tr>
<th>EQUALIZERS</th>
<th>Part Number</th>
<th>Data Rate (Mbps)</th>
<th>Launch Swing Compensation</th>
<th>Output Coupling (V)</th>
<th>No. of Inputs</th>
<th>No. of Outputs</th>
<th>Cable Length (m)</th>
<th>Temp Range (ºC)</th>
<th>Power (mW)</th>
<th>Package (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>12G  6G  3G  HD  SD</td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>GS12341</td>
<td>1–11880</td>
<td>YES</td>
<td>1.2–2.5</td>
<td>1</td>
<td>2</td>
<td>80 100 190 260 450</td>
<td>-40 to +85</td>
<td>405*</td>
<td>QFN-40 (6x4)</td>
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</tr>
<tr>
<td>GS12142</td>
<td>1–11880</td>
<td>YES</td>
<td>1.2–2.5</td>
<td>2</td>
<td>2</td>
<td>80 100 190 260 450</td>
<td>-40 to +85</td>
<td>385*</td>
<td>QFN-40 (6x4)</td>
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<tr>
<td>GS6042</td>
<td>125–5940</td>
<td>YES</td>
<td>1.2–3.3</td>
<td>1</td>
<td>1</td>
<td>– 80 210 300 550</td>
<td>-40 to +85</td>
<td>180</td>
<td>QFN-16 (4x4)</td>
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<tr>
<td>GS3241</td>
<td>1–2970</td>
<td>YES</td>
<td>1.2–2.5</td>
<td>1</td>
<td>2</td>
<td>– 190 260 450</td>
<td>-40 to +85</td>
<td>405*</td>
<td>QFN-40 (6x4)</td>
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<td>GS3140</td>
<td>1–2970</td>
<td>YES</td>
<td>1.0–2.5</td>
<td>1</td>
<td>1</td>
<td>– 200 280 500</td>
<td>-40 to +85</td>
<td>83</td>
<td>QFN-16-COL</td>
<td></td>
</tr>
</tbody>
</table>

*Power specifications include power consumed by integrated reclocker.
Cable Drivers

Integrated reclocking for the lowest output jitter, sophisticated, easy-to-use eye shaping for SMPTE compliance and data rates up to 11.88Gbps

MULTI-RATE
Semtech’s UHD-SDI cable drivers are SMPTE compliant and feature multi-rate operation; our high-performance GS12281 12G UHD-SDI cable driver is the industry’s best performing driver. Semtech offers a driver for every UHDTV-1, UHDTV-2, 3G, HD, and SD application.

INTEGRATED RECLOCKING, LOWEST JITTER
The GS12281’s integrated reclocker, with full bandwidth clock division, enables the best jitter performance at all rates up to 11.88Gbps.

ADVANCED SIGNAL INTEGRITY DIAGNOSTICS
The GS12281 features unique diagnostic capabilities, including pattern generation and error checking, and a high-resolution 3D eye monitor to ease board design and debugging while speeding time to market.

INPUT TRACE EQUALIZATION
The GS12281 features input trace equalization to compensate for long input trace lengths, which is specially tuned to work with unbalanced SDI signals. Input trace equalization is critical for optimizing performance in 12G UHD-SDI designs.

BEST OUTPUT RETURN LOSS
Output return loss performance that surpasses SMPTE specifications at all rates. This increased margin simplifies board optimization to speed production.

OUTPUT EYE SHAPING
The GS12281 features an output signal presence detector which provides status on the validity of the output. The GS12281 also offers high maximum output swing and sophisticated yet easy to use pre-emphasis to compensate for losses that occur after the cable driver output.

CABLE DRIVERS

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Data Rate (Mbps)</th>
<th>Power (mW)</th>
<th>Power Supply (V)</th>
<th>No. of Inputs</th>
<th>No. of Outputs</th>
<th>Input Trace EQ</th>
<th>Max Output Swing (mV)</th>
<th>ORL (dB)</th>
<th>Circuit Compatible with</th>
<th>Temp Range (ºC)</th>
<th>Package (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>GS12281</td>
<td>1–11880</td>
<td>360*</td>
<td>1.8, 2.5</td>
<td>1</td>
<td>2</td>
<td>YES</td>
<td>1000</td>
<td></td>
<td>GS12181 GS12081 GS3281 GS3590 GS12190</td>
<td>-40 to +85</td>
<td>QFN-40 (6x4)</td>
</tr>
<tr>
<td>GS12182</td>
<td>1–11880</td>
<td>360*</td>
<td>1.8, 2.5</td>
<td>2</td>
<td>2</td>
<td>YES</td>
<td>1000</td>
<td></td>
<td>GS12281 GS12181 GS12081 GS1281 GS3281</td>
<td>-40 to +85</td>
<td>QFN-40 (6x4)</td>
</tr>
<tr>
<td>GS12081</td>
<td>111880</td>
<td>170</td>
<td>1.8, 2.5</td>
<td>1</td>
<td>2</td>
<td>YES</td>
<td>1000</td>
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<td>GS12281 GS12181 GS12081 GS3281 GS3590 GS12190</td>
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<td>QFN-40 (6x4)</td>
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<tr>
<td>GS6081</td>
<td>143–5940</td>
<td>135</td>
<td>2.5 or 3.3</td>
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<td>2</td>
<td>YES</td>
<td>1800</td>
<td></td>
<td>GS2988</td>
<td>-40 to +85</td>
<td>QFN-16 (4x4)</td>
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<tr>
<td>GS6081</td>
<td>143–5940</td>
<td>205</td>
<td>2.5 or 3.3</td>
<td>1</td>
<td>4</td>
<td>YES</td>
<td>1800</td>
<td></td>
<td>GS2989</td>
<td>-40 to +85</td>
<td>QFN-16 (4x4)</td>
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<tr>
<td>GS3281</td>
<td>1–2970</td>
<td>375*</td>
<td>1.8, 2.5</td>
<td>1</td>
<td>2</td>
<td>YES</td>
<td>1000</td>
<td></td>
<td>GS12281 GS12181 GS12081 GS12090 GS3281 GS3590 GS12190</td>
<td>-40 to +85</td>
<td>QFN-40 (6x4)</td>
</tr>
</tbody>
</table>

*Power specifications include power consumed by integrated reclocker.
Reclockers

Reclocking is an important function required to maintain overall system robustness. As UHD-SDI infrastructure continues to expand and as broadcast products require intensified processing, overall system jitter can increase. Controlling overall system jitter is critical to reliable operation.

Processing devices such as FPGAs can have poor input jitter tolerance (IJT) and frequently require external reclocking to maintain overall system margin. The rapid growth in UHD-SDI infrastructure and the associated higher rates make reclocking mandatory in most applications. Semtech offers a complete family of UHD-SDI reclockers for all data rates. The GS12150 reclocker, with full bandwidth clock division, supports rates up to 11.88Gbps with low power consumption and is ideal for UHD-SDI applications.

**ADVANCED SIGNAL INTEGRITY FEATURES**

The GS12150 includes programmable trace equalization to compensate for high-frequency losses associated with board level interconnects. It operates without an external frequency reference. Programmable output swing and pre-emphasis provide flexibility for managing signal integrity of the output signals.

The GS12150 features unique diagnostic capabilities, including pattern generation and error checking, and a high-resolution 3D eye monitor to ease board design and debugging while speeding time to market.

**SMALL SIZE**

The GS12150 features a compact 6x4 40-pin QFN package, which is ideal for high-channel density designs or other applications where the layout is constrained by available PCB real estate.

**LOW POWER**

The GS12150’s low power consumption is ideal for designs with high channel density or in applications requiring low power consumption.

### RECLCOKERS

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Data Rate (Mbps)</th>
<th>Power Supply (V)</th>
<th>Integrated Eye Monitor</th>
<th>Output Jitter (UI)</th>
<th>Input Trace EQ</th>
<th>Output Pre-/De-emphasis</th>
<th>Input MUX</th>
<th>No. of Inputs</th>
<th>No. of Outputs</th>
<th>Temp Range (ºC)</th>
<th>Power (mW)</th>
<th>Package (mm)</th>
</tr>
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<tbody>
<tr>
<td>GS12150</td>
<td>11880 5940 2970 1485 270</td>
<td>1.8</td>
<td>YES</td>
<td>12G:0.08 6G:0.05 3G:0.04 HD:0.03 SD:0.03</td>
<td>YES</td>
<td>YES</td>
<td>2:1</td>
<td>2</td>
<td>2</td>
<td>-40 to +85</td>
<td>385</td>
<td>QFN-40 (6x4)</td>
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<tr>
<td>GS6151</td>
<td>5940 2970 1485 270</td>
<td>1.8</td>
<td>YES</td>
<td>6G:0.13 3G:0.09 HD:0.06</td>
<td>YES</td>
<td>YES</td>
<td>2:1</td>
<td>2</td>
<td>2</td>
<td>-40 to +85</td>
<td>130</td>
<td>QFN-32 (4x4)</td>
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<tr>
<td>GS6152</td>
<td>5940 2970 1485 270</td>
<td>1.8</td>
<td>YES</td>
<td>6G:0.13 3G:0.09 HD:0.06</td>
<td>YES</td>
<td>YES</td>
<td>4:1</td>
<td>4</td>
<td>2</td>
<td>-40 to +85</td>
<td>130</td>
<td>QFN-48 (6x6)</td>
</tr>
</tbody>
</table>
Configurable SDI Equalizer/Cable Driver

High-performance configurable devices for multi-rate applications up to 12Gbps

**DESIGN FLEXIBILITY**

The GS12190 is a low power, configurable 12G multi-rate reclocking cable equalizer and cable driver. The GS12190 is suitable for systems with limited connector space, and enables individual connectors to function either as an input or an output. In addition, the GS12190 is ideal for multi-function designs requiring connectors be reconfigurable based on customer needs.

Applications suitable for the GS12190 include next-generation 3G, 6G and 12G UHD-SDI infrastructures designed in support of UHDTV1, UHDTV2, 4K D-Cinema, 3D HFR, and HDR production image formats.

**ADVANCED SIGNAL INTEGRITY FEATURES**

The GS12190 features integrated reclocking with full bandwidth clock division, minimizing output jitter even at extended cable lengths. Its new design offers superb performance, even in the presence of UHD-SDI stress patterns. To compensate for PCB trace losses and to simplify PCB layout optimization, the GS12190 features output pre-emphasis. To facilitate optimal PCB layout, the GS12190 requires no external return loss components or return loss networks.

**CONFIGURABLE SDI EQUALIZER/CABLE DRIVER**

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Data Rate (Mbps)</th>
<th>Power (mW)</th>
<th>No. of Outputs</th>
<th>Cable Reach (m)</th>
<th>Integrated Reclocking</th>
<th>Circuit Compatible with</th>
<th>DVB-ASI and MADI</th>
<th>Temp Range (ºC)</th>
<th>Package (mm)</th>
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<tbody>
<tr>
<td>GS12190</td>
<td>1–11880</td>
<td>EQ: 430*</td>
<td>EQ: 1</td>
<td>12G: 70, 6G: 90</td>
<td>YES</td>
<td>NO - YES</td>
<td>-40 to +85</td>
<td>QFN-40 (6x4)</td>
<td></td>
</tr>
<tr>
<td></td>
<td></td>
<td>CD: 375*</td>
<td>CD: 1</td>
<td>3G: 170, HD: 240</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

*Power specifications include power consumed by integrated reclocker.

**ADVANCED SIGNAL INTEGRITY DIAGNOSTICS**

The GS12190 features unique diagnostic capabilities, including pattern generation and error checking, high-resolution 3D eye monitor to ease board design and debugging, and speed production time.

**SMALL PACKAGE SIZE**

The GS12190 features a compact 6x4 QFN package and low power consumption.

*Contact your local FAE for support designing a circuit for the GS12190, which supports drop-in compatibility for the GS3590.*
The GS12170 is an advanced bridge for HDMI 2.0 and UHD-SDI systems, enabling multiple conversions from HDMI to SDI, SDI to HDMI and rate conversions for SDI to SDI, all in a single, compact BGA device.

Furthermore, the GS12170 integrates digital audio insertion/extraction, along with other types of ancillary data.

The GS12170 dramatically lowers the cost and complexity of connecting HDMI to SDI, reduces the bill-of-materials, shrinks the board real estate, and accelerates time-to-market.

**SDI TO HDMI CONVERSION**

When converting from SDI to HDMI, the GS12170 offers seamless reception of SDI signals from HD through 12G, and converts them to the corresponding HDMI format.

Audio is automatically extracted from the SMPTE ST 299 audio data packets, converted to HDMI Audio sample packets and reinserted into the outgoing video. Optionally, external audio may be extracted/inserted using dedicated serial I/O pins.

The device also automatically converts embedded SMPTE ST 352 payload ID into CTA 861 InfoFrames to accurately transfer stream identification through the converted link.

**HDMI TO SDI CONVERSION**

When converting from HDMI to SDI, the GS12170 will accept all HDMI formats which can be carried on an SDI link operating at rates from HD to 12G, and convert them to the corresponding SMPTE-compliant format.

Audio is automatically extracted from the HDMI Audio sample packets, converted to SMPTE ST 299 audio data packets, and reinserted into the outgoing video. Optionally, external audio may be extracted/inserted using dedicated serial I/O pins.

The device also automatically converts CTA 861 InfoFrames into embedded SMPTE ST 352 payload ID to correctly carry stream identification through the converted link.

**SDI TO SDI RATE CONVERSION**

In addition to operation as an HDMI/SDI Bridge, the GS12170 continues to offer all the features of the original GS12070 UHD-SDI Gearbox, facilitating rate conversion for single-link and multi-link SDI interfaces from HD to 12G.

In addition to the GS12070 Gearbox features, the GS12170 adds the ability to insert/extract digital audio and other types of ancillary data.

**SPECIAL ANCILLARY DATA HANDLING**

The GS12170 includes a new high-speed, serial, ancillary data interface, enabling designs to handle large amounts of metadata.

Additionally, for designs requiring the output of SDI Timecode, a new dedicated core removes the complexity of inserting and maintaining timecode in SDI streams.
GS12170 HDMI/SDI Bridge

Single-chip format conversion between UHD-SDI and HDMI 2.0

<table>
<thead>
<tr>
<th>HDMI/SDI BRIDGE</th>
<th>Part Number</th>
<th>Data Rate (Mbps)</th>
<th>Number of Video Inputs</th>
<th>Number of Video Outputs</th>
<th>Number of Audio Input Channels</th>
<th>Number of Audio Output Channels</th>
<th>Temp Range (°C)</th>
<th>Package (mm)</th>
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</thead>
<tbody>
<tr>
<td>GS12170</td>
<td>270-11880</td>
<td>4</td>
<td>4</td>
<td>16 (8 stereo pairs)</td>
<td>16 (8 stereo pairs)</td>
<td>-40 to +85</td>
<td>BGA-196 (12x12)</td>
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</table>
SDI Transmitters/Serializers and Receivers/Deserializers

Choose the right SDI transmitter/serializer and SDI receiver/deserializer for your system

Designers can select components from different vendors, but when it comes to designs with SDI transmitters and SDI receivers, designers must first choose an architecture. In implementing a design with an SDI transmitter and/or SDI receiver, there are three architectural choices: Semtech’s complete SDI transmitter/SDI receiver solution architecture, an integrated-transceiver FPGA architecture and a FPGA-helper architecture. Let’s explore these three options with an assessment of the following key parameters: jitter, power consumption, integration (component/features), time-to-market, system size, and cost.

SEMTECH’S COMPLETE SOLUTION ARCHITECTURE

Leveraging our expertise in signal integrity and our deep understanding of broadcast video technologies, Semtech’s SDI transmitter and SDI receiver offering encapsulates all the analog components (SerDes, VCO, CD, EQ, and Reclocker) and digital SMPTE video and audio processing required to transmit and receive SDI video. Integrating all of these components into one package reduces the PCB footprint required to implement SDI transmit/receive, and the solution benefits from Semtech’s superior jitter performance. This optimized, cost effective and power efficient ASIC implementation allows customers to focus on their unique value-added processing for quicker time-to-market. Only Semtech offers a solution that scores high for each evaluation parameter.

INTEGRATED-TRANSCIEVER FPGA ARCHITECTURE

Integrated-transceiver FGAs typically offer the worst specifications in terms of jitter. Maximum output jitter and input jitter tolerance (IJT) are typically at the limit of the SMPTE standards and, in some cases, actually in violation of industry norms. That is why extra components, namely VCXOs and reclockers, are required to reduce system jitter performance to an acceptable level. This comes at a penalty of higher power consumption, system footprint size and cost. Because of the fine tuning required to enable functioning and because of the IP licensing/development required for the digital SMPTE video processing, this architecture unnecessarily prolongs time-to-market. Finally, while FGAs integrate transceivers, they do not integrate routing components like cable drivers and equalizers.

FPGA-HELPER ARCHITECTURE

The FPGA-helper architecture, as depicted below, involves the use of a component that includes the physical media attachment part of a SMPTE SDI receiver/SDI transmitter with the digital SMPTE processing implemented in the FPGA. The result is an architecture that is taxing in terms of power consumption, and those FPGA-helper parts are lacking even basic SMPTE digital processing.

In many cases, product specific FPGA IP already requires high utilization factors in small, low-cost FGAs, and the added requirement of digital SMPTE video processing in the FPGA may drive adoption of a larger FPGA. This results in further penalties in power consumption, size and system cost. And while this architecture can improve system jitter performance, the added engineering effort in developing (or licensing) and stitching that video processing logic to product specific code allows a slower time-to-market. Finally, while some FPGA-helper parts integrate a cable driver, the offering lacks an integrated equalizer.

HOW THE SEMTECH SOLUTION STACKS UP

Compare the ratings of each of the three architectures for key parameters in an implementation of 1Rx and 1Tx channel.

<table>
<thead>
<tr>
<th>Parameter</th>
<th>Semtech Complete Solution</th>
<th>Integrated Transceiver FPGA</th>
<th>FPGA-Helper SerDes</th>
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<tbody>
<tr>
<td>Jitter Performance</td>
<td>🟢</td>
<td>🟢</td>
<td>🟢</td>
</tr>
<tr>
<td>Time-to-Market</td>
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<td>🟢</td>
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<td>Power Consumption</td>
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<td>Integration</td>
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<td>Overall PCB Space</td>
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<tr>
<td>System Cost</td>
<td>🟢</td>
<td>🟢</td>
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</table>
SDI Transmitters/Serializers

High-performance, integrated, low power solution for SDI transmit links

3Gbps SDI
Semtech’s GS2972 and GS2962 are fully compatible with SMPTE ST 424, ST 425, ST 2081-1, and ST 2082-1 UHD-SDI standards. In addition, Semtech SDI transmitters feature integrated SMPTE video processing including scrambling, TRS detection, insertion, and ancillary data insertion.

Semtech’s GS2972 SDI transmitter includes an integrated audio embedder and supports AES, I2S and serial audio formats. The fully integrated functionality of Semtech’s SDI transmitters provides the lowest power and smallest footprint solution for a complete SDI transmit link.

COMPLETE VIDEO PROCESSING
Semtech SDI transmitters offer the most complete video processing features available. These include, but are not limited to, scrambling, TRS detection or insertion, and ancillary data insertion.

GREAT FOR DVB-ASI
All Semtech SDI transmitters are DVB-ASI capable.

AUDIO EMBEDDING
Semtech’s SDI transmitters include an integrated audio embedder supporting AES, I2S and serial audio formats.

EXCELLENT JITTER PERFORMANCE
Due to the integrated PLL with narrow loop bandwidth, the SDI transmitters can reject up to 300ps of jitter on the parallel clock, outputting very low jitter, SMPTE-compliant SDI signals.

POWER AND AREA EFFICIENT
The high level of integration in Semtech’s SDI transmitters enables low power, low profile SDI link implementation.

APPLICATIONS
Cameras, camera control units, multi viewers, routers, production switchers, master control switchers, VTRs, video servers, encoders/decoders, up/down/cross converters, audio embedders, format detectors, test and measurement equipment.

### SDI TRANSMITTERS

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Data Rate (Mbps)</th>
<th>Video Processing</th>
<th>DVB/ASI</th>
<th>Ancillary Data Insert</th>
<th>Audio Embed</th>
<th>Cable Driver</th>
<th>Output Jitter (ps)</th>
<th>Parallel Bus Width</th>
<th>CEA 861 Timing</th>
<th>Temp Range (ºC)</th>
<th>Power (mW)</th>
<th>Package</th>
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<td>GS2972</td>
<td>270</td>
<td>YES</td>
<td>YES</td>
<td>YES</td>
<td>YES</td>
<td>3G:40</td>
<td>3G:40 HD:50 SD:200</td>
<td>10 or 20</td>
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<td>-40 to +85</td>
<td>400</td>
<td>BGA 100</td>
</tr>
<tr>
<td>GS2972</td>
<td>1485</td>
<td>YES</td>
<td>YES</td>
<td>YES</td>
<td>NO</td>
<td>3G:40</td>
<td>3G:40 HD:50 SD:200</td>
<td>10 or 20</td>
<td>YES</td>
<td>-40 to +85</td>
<td>350</td>
<td>BGA 100</td>
</tr>
</tbody>
</table>

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11
SDI Receivers/Deserializers

High-performance, integrated, low power solution for SDI receive links

3Gbps SDI

The GS3470 and GS3471 are Semtech’s latest generation high-performance, single-chip SDI receiver solutions, featuring integrated cable equalization, video and audio processing and dual SDI inputs. All Semtech’s SDI receivers are fully compatible with SMPTE ST 424, ST 425, ST 2081-1, and ST 2082-1 UHD-SDI standards. The GS3470 and GS3471 feature two selectable serial video inputs which make them ideal for video monitoring applications. Both parts are optimized for low power designs, providing up to 45 percent power reduction over previous generation SDI receivers.

INTEGRATED EQUALIZER

The GS3471 fully integrates a high-performance long reach adaptive cable equalizer, providing up to 200m reach at 3Gbps. The integration of the cable equalizer reduces overall PCB component footprint and power.

COMPLETE VIDEO PROCESSING

Semtech’s SDI receivers offer integrated SMPTE video processing including descrambling, word alignment, comprehensive error detection and correction and ancillary data extraction. Additionally, all Semtech SDI receivers fully support DVB-ASI inputs.

AUDIO DE-EMBEDDING

The GS3470 and GS3471 include an integrated SMPTE compliant audio de-embedder, supporting AES, P5 and serial digital audio output formats. Both parts feature a high performance audio clock generator, further reducing overall system costs.

APPLICATIONS

Monitors, camera control units, multi- viewers, routers, production switchers, master control switchers, video recorders and servers, compression encoders and decoders, video format converters, audio de-embedders, test and measurement equipment.

---

**SDI Receivers**

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Data Rate (Mbps)</th>
<th>Integrated Equalizer</th>
<th>Number of SDI Inputs</th>
<th>Audio De-embed</th>
<th>Audio Clock Generator</th>
<th>I/O Supply Voltage (V)</th>
<th>Power (mW)</th>
<th>Package (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>GS3471</td>
<td>270, 1485, 2970</td>
<td>Yes</td>
<td>2</td>
<td>Yes</td>
<td>Yes</td>
<td>1.8 or 2.5</td>
<td>300</td>
<td>BGA 100 (9x9)</td>
</tr>
<tr>
<td>GS3470</td>
<td>270, 1485, 2970</td>
<td>No</td>
<td>2</td>
<td>Yes</td>
<td>Yes</td>
<td>1.8 or 2.5</td>
<td>220</td>
<td>BGA 100 (9x9)</td>
</tr>
<tr>
<td>GS2971A</td>
<td>270, 1485, 2970</td>
<td>Yes</td>
<td>1</td>
<td>Yes</td>
<td>Yes</td>
<td>1.8 or 3.3</td>
<td>525</td>
<td>BGA 100 (11x11)</td>
</tr>
<tr>
<td>GS2961A</td>
<td>270, 1485, 2970</td>
<td>Yes</td>
<td>1</td>
<td>No</td>
<td>No</td>
<td>1.8 or 3.3</td>
<td>500</td>
<td>BGA 100 (11x11)</td>
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<tr>
<td>GS2970A</td>
<td>270, 1485, 2970</td>
<td>No</td>
<td>1</td>
<td>Yes</td>
<td>Yes</td>
<td>1.8 or 3.3</td>
<td>350</td>
<td>BGA 100 (11x11)</td>
</tr>
<tr>
<td>GS2960A</td>
<td>270, 1485, 2970</td>
<td>No</td>
<td>1</td>
<td>No</td>
<td>No</td>
<td>1.8 or 3.3</td>
<td>320</td>
<td>BGA 100 (11x11)</td>
</tr>
</tbody>
</table>
**SDI Crosspoints**

Semtech crosspoint switches provide an unmatched combination of size, features and performance.

### MULTIPLE STROBES

Eight update enabled strobe pins allow our crosspoint to be partitioned into independently updatable blocks. This is useful in multi-format environments when the switches need to occur at different points in time. Each update strobe can be assigned on a per-output basis, enabling maximum flexibility.

### DATA RATE MARGIN

Supporting data rates up to 3.5Gbps means that there is significant margin for video systems operating up to 2.97Gbps. This margin also means the product can be used in Xaui™, DisplayPort™ and HDMI switching applications.

### FULLY INDEPENDENT INPUT AND OUTPUT CHANNELS

All our crosspoints provide independent input trace equalization and output de-emphasis, which can compensate for over 50 inches of PCB trace loss. This provides higher signal integrity and lower jitter in designs utilizing long traces or passive splitting. In addition, with output swing configurable as low as 200mV, system power can be significantly reduced.

### ON-CHIP PATTERN GENERATORS

**EYE PATTERN DIAGNOSTICS**

Independent pattern generators and checkers can be used for testing signal paths on either the input or output side of the system. The pattern checker can be configured to check for bit errors using one of three PRBS patterns or any arbitrary pattern, which is critical for evaluating system performance with video pathological signals. All crosspoint devices can check for bit errors at arbitrary phase offsets from the received data, and jitter margin can be determined by measurement of the horizontal eye opening.

### TEMPERATURE SENSORS

Four on-chip temperature sensors monitor the junction temperature of the chip. This enables automated control of fan speed and power down sequences to meet energy conservation requirements.

### LOW POWER

When compared to competitive solutions, Semtech crosspoints provide industry leading per channel power consumption, with less than 1W consumed in standby mode. System power can be reduced further by the crosspoint’s high input sensitivity while using Semtech equalizers configured for minimum output swing levels.

### FLEXIBLE FOOTPRINT

All Semtech crosspoints are pin compatible, sharing the same control interface and register set. This allows a single platform design to be easily scaled to the necessary switch size with no re-work required in either hardware or software.

### INPUT SENSITIVITY

To address losses typical in many router architectures, the Semtech crosspoint family offers high input sensitivity, resulting in additional system margin. High input sensitivity facilitates system optimization, including reduced swing on upstream drivers, providing substantial power savings.

### APPLICATIONS

Routers, multi-viewers, production switchers, master control switchers, and broadcast modular equipment.

### CROSSPOINT SWITCHES

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Data Rate (Gbps)</th>
<th>Inputs</th>
<th>Input Sensitivity (mV)</th>
<th>Input Trace EQ</th>
<th>Outputs</th>
<th>Output De-emphasis</th>
<th>DC Coupling</th>
<th>Temp Range (°C)</th>
<th>Power (W)</th>
<th>Package (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>GX3290</td>
<td>3.5</td>
<td>290</td>
<td>80</td>
<td>YES</td>
<td>290</td>
<td>YES</td>
<td>1.2V, 1.8V, 2.5V</td>
<td>0 to +85</td>
<td>34</td>
<td>BGA 2377 (50x50)</td>
</tr>
<tr>
<td>GX3190</td>
<td>3.5</td>
<td>146</td>
<td>80</td>
<td>YES</td>
<td>290</td>
<td>YES</td>
<td>1.2V, 1.8V, 2.5V</td>
<td>0 to +85</td>
<td>25</td>
<td>BGA 2377 (50x50)</td>
</tr>
<tr>
<td>GX3246</td>
<td>3.5</td>
<td>290</td>
<td>80</td>
<td>YES</td>
<td>146</td>
<td>YES</td>
<td>1.2V, 1.8V, 2.5V</td>
<td>0 to +85</td>
<td>18</td>
<td>BGA 2377 (50x50)</td>
</tr>
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<td>GX3202</td>
<td>3.5</td>
<td>202</td>
<td>80</td>
<td>YES</td>
<td>202</td>
<td>YES</td>
<td>1.2V, 1.8V, 2.5V</td>
<td>0 to +85</td>
<td>24</td>
<td>BGA 2377 (50x50)</td>
</tr>
<tr>
<td>GX3146</td>
<td>3.5</td>
<td>146</td>
<td>80</td>
<td>YES</td>
<td>146</td>
<td>YES</td>
<td>1.2V, 1.8V, 2.5V</td>
<td>0 to +85</td>
<td>18</td>
<td>BGA 2377 (50x50)</td>
</tr>
</tbody>
</table>

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Timing

Complete timing and genlock for broadcast video

GENLOCK CAPABILITY
GS4900B/GS4901B/GS4910B/GS4911B clock generators can be genlocked to reference with a variable offset. On loss of reference, the video clocks will flywheel to maintain their frequency.

GENERTATES MULTIPLE CLOCKS
Three video clocks (two single-ended and one differential), three single-ended audio clocks and eight single-ended configurable timing signals can be generated. Video clocks up to 165MHz can support up to 3Gbps SDI and UXGA. In addition, the GS4911B features crosslocking capability where HD timing can be generated from an SD reference, increasing design flexibility.

INPUT STANDARD DETECTION
By supplying the clock generator with HSync, VSync and FSync, the chip will determine if it matches one of 36 video or 16 graphics standards and will report the detected standard.

REDUCES JITTER TO MEET SMPTE SPECS
The GS4915 ClockCleaner™ will reduce jitter on video clocks of 27, 74.25, 74.25/1.001, 148.5, and 148.5/1.001MHz. Output jitter will typically be 20ps, which will guarantee 3Gbps SDI compliance when used with a SDI transmitter.

FLEXIBLE I/O FREQUENCIES AND LEVELS
SD (27MHz) and HD (74.25, 74.25/1.001, 148.5, and 148.5/1.001MHz) frequencies can be input and output from the GS4915. The chip also has single-ended and differential inputs and outputs to facilitate interfacing with a variety of chips.

FREQUENCY DOUBLER TO MEET 3Gbps SDI REQUIREMENTS
If the input clock frequency is 74.25 or 74.25/1.001MHz, the GS4915 can double the output, providing a low jitter 148.5 or 148.5/1.001MHz output clock which can be used for HD-SDI and 3Gbps SDI applications.

CLOCK GENERATORS

<table>
<thead>
<tr>
<th>Part Number</th>
<th>Input Video Standard</th>
<th>Output Video Standard</th>
<th>Max Output Video Clock (MHz)</th>
<th>Power Supply (V)</th>
<th>Genlock</th>
<th>Audio Clocks</th>
<th>User Programmable</th>
<th>Power (mW)</th>
<th>Package (mm)</th>
</tr>
</thead>
<tbody>
<tr>
<td>GS4900B</td>
<td>3G/HD/SD/ Graphics</td>
<td>SD</td>
<td>54</td>
<td>3.3, 1.8</td>
<td>YES</td>
<td>NO</td>
<td>NO</td>
<td>215</td>
<td>QFN-64 (9x9)</td>
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<tr>
<td>GS4901B</td>
<td>3G/HD/SD/ Graphics</td>
<td>SD</td>
<td>54</td>
<td>3.3, 1.8</td>
<td>YES</td>
<td>YES</td>
<td>NO</td>
<td>265</td>
<td>QFN-64 (9x9)</td>
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<tr>
<td>GS4910B</td>
<td>3G/HD/SD/ Graphics</td>
<td>3G/HD/SD/ Graphics</td>
<td>165</td>
<td>3.3, 1.8</td>
<td>YES</td>
<td>NO</td>
<td>YES</td>
<td>250</td>
<td>QFN-64 (9x9)</td>
</tr>
<tr>
<td>GS4911B</td>
<td>3G/HD/SD/ Graphics</td>
<td>3G/HD/SD/ Graphics</td>
<td>165</td>
<td>3.3, 1.8</td>
<td>YES</td>
<td>YES</td>
<td>YES</td>
<td>300</td>
<td>QFN-64 (9x9)</td>
</tr>
</tbody>
</table>
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